525 Rec'd PCT/PTO 05 JAN 2007

Form PTO-1390 (REV 10-95) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTORNEY'S DOCKET NUMBER 3135-010012

TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. 371

INTERNATIONAL APPLICATION NO PCT/NL99/00424

INTERNATIONAL FILING DATE 05.07.99 (July 05, 1999)

PRIORITY DATES CLAIMED **06.07.98 (July 06, 1998)** 

TITLE OF INVENTION

## MOULD, ENCAPSULATING DEVICE AND METHOD OF ENCAPSULATION

APPI	LICANT(S) FOR DO/EO/US  Marcel Gerardus Antonius TOMASSEN and Antonius Bernardus CLAASSEN .
	cant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items therefore the information
1. 🗵	This is a FIRST submission of items concerning a filing under 35 U.S.C. 371
2. □	This is a SECOND or SUBSEQUENT submission of items concerning a filing under 35 U.S.C 371
3.	This express request to begin national examination procedures (35 U S.C. 371(f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 U S C 371(b) and PCT Articles 22 and 39(1)
4. <b>区</b>	A proper Demand for International Preliminary Examination was made by the 19th month from the earliest claimed priority date
5. <b>E</b>	A copy of the International Application as filed (35 U S C 371(c)(2))
	a
	b 🖾 has been transmitted by the International Bureau
	c  us not required, as the application was filed in the United States Receiving Office (RO/US).
6. C	A translation of the International Application into English (35 U S C 371(c)(2))
7.	Amendments to the claims of the International Application under PCT Article 19 (35 U S C 371(c)(3))
	a are transmitted herewith (required only if not transmitted by the International Bureau)
	b have been transmitted by the International Bureau
	c  have not been made; however, the time limit for making such amendments has NOT expired
	d 🖾 have not been made and will not be made
8. E	A translation of the amendments to the claims under PCT Article 19 (35 U S C 371(c)(3))
9. [	An oath or declaration of the inventor(s) (35 U S C. 371(c)(4))
10	A translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U S C 371(c)(5))
Item	s 11. to 16. below concern document(s) or information included:
11	An Information Disclosure Statement under 37 CFR 1.97 and 1 98
12	An assignment document for recording A separate cover sheet in compliance with 37 CFR 3 28 and 3 31 is included.
13	A FIRST preliminary amendment together with retyped specification pages 1-3a
	A SECOND or SUBSEQUENT preliminary amendment
14	□ A substitute specification
15	A change of power of attorney and/or address letter
	Other items or information:  a. WO 00/02242-Front Page with Abstract, Specification, Claims, Drawings and Search Report (14 pp.)  b. International Preliminary Examination Report and Annex (14 pp.)
page	1 of 2 (January 1990

# 500 Rec'd PCT/PTO 0 5 JAN 2001

U.S APPLICATION NO	43155	INTERNATIONAL APPLICATION NO PCT/NL99/00424			ATTORNEY'S DOCKET NUMBER 3135-010012		
17. The following	fees are submitted: TEE (37 CFR 1.492(a)(1)-(5)):			CALC	CULATIONS P	TO USE ONLY	
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CLAIMS	NUMBER FILED	NUMBER EXTRA	RATE				
Total claims	8 - 20	0	X \$18.00	\$	0.00		
Independent claims	3 - 3 =	0	X \$78.00	\$	0.00		
MULTIPLE DEPENDEN	TT CLAIM(S) (if applicable)		+ \$260.00	\$	0.00		
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Pittsburgh, Pennsyl	W. McI	Ivaine					
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# 500 Rec'd PCT/PTO 0 5 JAN 2001

PATENT APPLICATION/PCT Attorney Docket 3135-010012

#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Marcel Gerardus Antonius TOMASSEN

and Antonius Bernardus CLAASSEN

International Application

No. PCT/NL99/00424

International Filing Date

05 July 1999

Priority Date Claimed

06 July 1998

Serial No. Not Yet Assigned

Filed Concurrently Herewith

MOULD, ENCAPSULATING

**DEVICE AND METHOD OF** 

**ENCAPSULATION** 

Pittsburgh, Pennsylvania

January 5, 2001

## PRELIMINARY AMENDMENT

Assistant Commissioner for Patents Washington DC 20231

Sir:

Prior to initial examination, please amend the above-identified patent application as follows:

### **IN THE SPECIFICATION:**

Original specification pages 1-6 were amended during Chapter II proceedings in a letter dated July 7, 2000. The following further amendments are being made to simply conform the application to standard United States Patent practice.

## Marcel Gerardus Antonius TOMASSEN et al.

Amended page 1, after the title and at line 2, insert the following headings:

#### -- BACKGROUND OF THE INVENTION

#### 1. Field of the Invention--.

Amended page 1, line 4, delete ", according to preamble of claim 1".

Amended page 1, line 5, delete "according the preamble of claim 6".

Amended page 1, line 12, insert the following heading:

### -- 2. Description of the Prior Art--.

Amended page 2, line 9, insert the following heading:

## --SUMMARY OF THE INVENTION--.

Amended page 2, line 10, delete "according claim 1".

Amended page 3, line 28, delete "according claim 6".

Amended page 4, line 4, change "Tis" to read --This--.

Amended page 4, line 17, delete "according claim 7".

Amended page 4, line 19, after "Preferably" insert a comma (,).

Amended page 4, line 19, delete "is".

Amended page 4, line 20, before "applied" insert --is--.

Amended page 4, line 28, insert the following heading:

## --BRIEF DESCRIPTION OF THE DRAWING--.

Amended page 5, before line 1, insert the following heading:

### --DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS--.

#### **IN THE CLAIMS**:

Please cancel original claims 1-12 and cancel amended claims 1-8. Amended claims 1-8 have been rewritten as claims 13-20 as follows:

### Marcel Gerardus Antonius TOMASSEN et al.

--13. A mould for encapsulating electronic components mounted on a carrier, comprising:

at least two mould parts displaceable relative to each other, at least one of which is provided with a recess, and

feed means for encapsulating material,

wherein at least one of the mould parts is provided with a runner which connects on one side to a wall of a mould part co-defining a mould cavity and connects on the other side to a side of the mould part remote from the mould cavity,

wherein the runner takes a multiple form that connects to a number of apertures 10 in the wall of a lower mould part on positions opposite the recess in the upper mould part.

- 14. The mould as claimed in claim 13, wherein a plurality of runners connecting onto the wall defining the mould cavity is in mutual communication and is connected to a single runner which connects onto a side of the lower mould part remote from the mould cavity.
- 15. The mould as claimed in claim 13, wherein the runner debouches in a wall defining a mould cavity, which wall is screened from a feed opening for encapsulating material by a carrier when encapsulating material is fed to the mould.
- 16. The mould as claimed in claim 13, wherein the apertures are arranged in the wall of a lower mould part in patterns.
- 17. The mould as claimed in claim 13, wherein the lower mould part is provided with at least one aligning edge for positioning a carrier relative to the lower mould part.

#### Marcel Gerardus Antonius TOMASSEN et al. 4

18. An encapsulating device for encapsulating electronic components mounted on a carrier, comprising:

a mould as claimed in claim 13,

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drive means for positioning and causing the mould parts to move relative to each 5 other,

feed means for encapsulating material, and

a fan connecting onto the side of the runner remote from the mould cavity, wherein the fan is adapted to blow gases into the runner and to extract gases from the runner.

- 19. A method for encapsulating electronic components mounted on a carrier, using a mould defining a mould cavity and a runner connecting onto a wall co-defining the mould cavity, comprising the steps of:
  - A) positioning at least one carrier relative to a lower mould part,
- B) closing the mould by moving the lower mould part and an upper mould part towards each other,
- C) feeding encapsulating material to the part of the mould cavity left clear by the carrier, and
- D) opening the upper and lower mould parts and removing the carrier with 10 encapsulating material arranged thereon, wherein during step D) an overpressure is applied in the runner which overpressure releases the encapsulated electronic components from the lower mould part.
  - 20. The method for encapsulating electronic components according to claim 19, wherein after step A) during step B) an underpressure is applied in the runner, whereby the carrier is sucked to the lower mould part.--

Marcel Gerardus Antonius TOMASSEN et al.

IN THE ABSTRACT:

After the claims, please insert a page containing the Abstract Of The Disclosure,

which is attached hereto as a separately typed page.

**REMARKS** 

The specification has been amended by this Preliminary Amendment to place the

application in conformance with standard United States Patent practice.

Original claims 1-12 have been cancelled and amended claims 1-8 have been

cancelled and rewritten as claims 13-20 in order to eliminate the multiple dependencies and

to conform the claims to standard United States patent practice. Claims 1-8, inserted in the

application during Chapter II proceedings, have been cancelled in favor of new claims 13-20.

Claims 1-8 were not cancelled for the purpose of overcoming prior art or to correct any

perceived deficiencies in the claims with respect to 35 U.S.C. §112. Rather, claims 1-8 were

cancelled simply to facilitate removal of reference numerals from the claims and to eliminate

multiple dependencies.

An Abstract Of The Disclosure has been added as a separately typed page to be

inserted after the claims.

Examination and allowance of claims 13-20 are respectfully requested.

Respectfully submitted,

WEBB ZIESENHEIM LOGSDON ORKIN & HANSON, P.C.

Rv

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# MOULD, ENCAPSULATING DEVICE AND METHOD OF ENCAPSULATION

#### ABSTRACT OF THE DISCLOSURE

The invention relates to a mould for encapsulating electronic components mounted on a carrier, comprising: at least two mould parts displaceable relative to each other, at least one of which is provided with a recess, and feed means for encapsulating material, wherein at least one of the mould parts is provided with a runner which connects on one side to a wall of a mould part co-defining a mould cavity and connects on the other side to a side of the mould part remote from the mould cavity. The invention also relates to an encapsulating device of which such a mould forms parts, and to a method for encapsulating electronic components mounted on a carrier.

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#### Mould, encapsulating device and method of encapsulation

The invention relates to a mould for encapsulating electronic components mounted on a carrier, according the preamble of claim 1. The invention also relates to an encapsulating device according the preamble of claim 6. Finally, the invention also relates to a method for encapsulating electronic components mounted on a carrier.

Such a mould, encapsulating device and method form known art with which, using for instance epoxy resin, a package of encapsulating material can be applied round an electronic component arranged on a carrier. This technique is used particularly for encapsulating semiconductor products, such as for instance chips.

The European patent application EP 0 730 937 discloses a resin moulding machine with release film. The machine is provided with a mechanism that fixes release film on inner faces of the moulding sections and clamping faces. Also disclosed are air sucking holes in the upper die to hold substrates in the upper die. The loading of substrates in the machine with air sucking holes in the upper die takes a lot of time. The apparatus is also not suited for lead frames with wire bounded electronic components.

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There is a tendency in the market towards arranging packages of encapsulating material on a carrier which are increasingly larger in terms of surface area. During further processing these can be for instance sawn into separate parts. This technique is also known as "C.S.P.", which is derived from "chip size packaging". The existing moulds and methods have the drawback that when the mould parts are closed a part of the electronic components can come into contact with a mould part. This can for instance cause damage to the components and/or contact wires ("leads") can be deformed. This is the particular consequence of a carrier not being completely flat when it is placed in a mould. Particularly carriers on which larger packages of encapsulating material must be arranged so that a plurality of electronic components can be moulded simultaneously with one package have such a three-dimensional form. The reason for this is that these carriers are in general thermally treated at many

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points during placing of the electronic components. This results in stresses in the carrier which, when it is placed in a mould, can result in the carrier not being completely flat. As described above, this results in possible damage to the electronic components and/or connecting wires.

The present invention has for its object to provide an improved mould, encapsulating device and method for encapsulating electronic components mounted on a carrier, with which damage to electronic components and contact wires can be prevented.

The invention provides for this purpose a mould according claim 1. With such a mould a carrier can be placed on a simple way on the lower mould part and sucked against the lower mould part before closing of the mould. An important advantage over the prior art is that the carrier is easier to place on a bottom wall of the lower mould part than in an upper mould part. As of the number of apertures in the mould the carrier lies more or less flat against the lower mould part. The force of gravity thus co-acts with the suction through the runner, which makes it easier to position the carrier more or less flat. Thus is prevented that, when the mould is closed, electronic components and/or contact wires come into contact with a countermould part because the carrier is not flat. Especially where contact of components with the upper mould can occur, on positions opposite the recess in the upper mould, the carrier is due to the location of the apertures to be sucked to the lower mould. Avoiding this contact means that damage to electronic components and/or contact wires is prevented. The number of apertures onto which runners connect makes that the carrier can be placed in flat position on a mould part with great precision. This increases the reliability of the mould. In a preferred embodiment 2 plurality of runners connecting onto the wall co-defining the mould cavity are in mutual communication and are connected to a single runner which connects onto a side of the mould part remote from the mould cavity. By extracting gases through a single runner it can thus be achieved that the carrier is adhered by suction at a plurality of positions. A mould with great reliability can thus be realized with a simple suction.

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The Japanese patent JP 06 021120 discloses a moulding apparatus for mounting parts carrying semiconductor chips. The bottom of a cavity of a lower metal mould has a sucking hole C to prevent deviation of the mounting part in lateral direction and to improve the yield rate. This apparatus is only suited for processing special products with a tip part that is held between a lower and an upper mould and a mounting part that is located below the tip part. As the apparatus possesses only one sucking hole the apparatus is also not suited for placing a mounting part in a flat position when the mounting part was deformed before processing in the apparatus.

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The runner preferably debouches in a wall defining a mould cavity, which wall is screened from the feed opening for encapsulating material by the carrier when encapsulating material is fed to the mould. In order to prevent encapsulating material entering the runner, the runner and feed opening for encapsulating material must be placed relative to each other such that during the encapsulating they are mutually separated by a carrier. It is noted here that it is recommended that the wall defining a mould cavity is formed by the bottom of the lower mould part.

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In yet another preferred embodiment the lower mould is also provided with at least one aligning edge for positioning a carrier relative to the mould part. Since it can be difficult to displace a carrier again relative to a mould part once the former has been adhered by suction by the runner, it is recommended to first place the carrier at the correct position and then to activate the suction action by means of the runner. Another option is to activate the suction action of the runner immediately on placing, and in this case it is recommended to place the carrier directly at the correct position in the mould part. The aligning edge is also useful here.

The invention also comprises an encapsulating device according claim 6. For suction of the carrier by means of the runner it is necessary for an underpressure to be applied in the runner. This is realized by means of the fan which extracts gases. A carrier can thus also remain sucked against a mould part when the mould is opened, whereby the carrier with encapsulating material can be "pulled loose" from an opposite mould part.

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This opposite mould part (for instance the top mould) can thus be embodied without ejector pins, which makes the construction of the upper mould more simple. The facility of blowing gases into the runner makes that an overpressure in the runner can be realised. Tis makes it possible to exert a force on the carrier with such that the carrier is released from the lower mould part. It is thus possible to embody the mould in simplified manner compared to the moulds applied in the prior art. These after all employ ejector pins for pressing a carrier out of the mould after applying of the encapsulating material. It will be apparent that a mould incorporating a number of ejector pins has an emphatically more complex structure than a mould in which these can be omitted or arranged in more limited number. An additional advantage of blowing gases into the runner is that in this manner the runner can be cleaned of possible contaminants. It is thus also recommended to apply at least limited over-pressure in the runner during cleaning of the mould so that no contaminants can be brushed or otherwise carried into the runner.

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The invention also relates to a method for encapsulating electronic components according claim 7. By means of this method the carrier can be placed in close-fitting connection against a wall of a mould part before the mould is closed. This results in the advantages as already described above. Preferably is after step A) during a step B) an under-pressure applied in the runner (9), whereby the carrier is sucked to lower mould part (3). By means of this method the carrier can be placed in close-fitting connection against a wall of a mould part before the mould is closed. This results in the advantages as already described above. With this method two advantages as described above are combined. That is, the carrier can be arranged close-fittingly in a mould part and, after the encapsulating process, the carrier will be released from the relevant mould part by overpressure in the runner. The advantages of both methods are hereby also combined.

The present invention will be further elucidated with reference to the non-limitative embodiment shown herein below. The figure shows a view of a schematic three-dimensional representation of a mould according to the invention in partly exploded situation.

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The figure shows a mould 1 consisting of an upper mould part 2 and a lower mould part 3. Lower mould part 3 is provided with a central beam 4 in which recesses 5 are arranged for placing pellets of encapsulating material. Also situated in central beam 4 in each recess 5 are plungers (not shown) with which pressure can be applied to the encapsulating material.

Support plates 6,7 are arranged on either side of central beam 4. Support plate 7 is shown in a detached position while support plate 6 is shown in an assembled position. Support plates 6,7 are provided with a large number of apertures 8 which are placed together in groups. These apertures 8 connect onto runners 9 which run through support plates 6,7 and which are only shown for a few of the apertures 8 by means of broken lines. These runners 9 connect onto tracks 10 arranged in a base block 11. Base blocks 11 are adapted to bear support plates 6,7. Each base block 11 is provided with an internal bore 12 which connects onto tracks 10. Base blocks 11 are each provided with an outlet 13 of the bores 12. By now connecting a fan (not shown) onto each outlet 13, air can be drawn in through bore 12, tracks 10, runners 9 and finally apertures 8. The suction of air through apertures 8 can be utilized to place catriers such as for instance lead frames or B.G.A's in flat position on the support plates 6,7. It is also possible to release carriers from support plates 6,7 by increasing the pressure close to the outlet 13. This increased pressure will be transmitted to the apertures 8, which at least simplifies release of the carriers.

It is noted that the groups of apertures 8 are placed such that in terms of shape they correspond with the shape of the spaces 14 which are left clear in the contact side 15 of the upper mould part 2. These spaces 14 will be filled with encapsulating material, which implies that the carriers will contain electronic components precisely at that position where spaces 14 are situated. It is therefore important that the carrier is in flat position particularly at these positions. It is for this reason that the apertures 8 are arranged at precisely these positions.

Although the invention is elucidated with reference to only a single embodiment, it will be apparent to all that the invention is in no way limited to the described and shown embodiment. On the contrary, many more variations are possible for the

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skilled person within the scope of the invention. It is thus possible for instance to arrange apertures 8 also, or only, in the upper mould part 2, whereby release of a carrier provided with encapsulating material can be realized by applying an overpressure on apertures 8. In addition, there are numerous possible variations in respect of the number and positions of apertures 8.

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#### Claims

mould cavity (10),

- 1. Mould (1) for encapsulating electronic components mounted on a carrier, comprising:
- at least two mould parts (2,3) displaceable relative to each other, at least one of which is provided with a recess (5), and
  - feed means for encapsulating material,
    wherein at least one of the mould parts (2,3) is provided with a suction runner (9)
    which connects on one side to a wall of a mould part (2,3) co-defining a mould cavity
    (10) and connects on the other side to a side of the mould part (2,3) remote from the

characterised in that the suction runner (9) takes a multiple form that connects to a number of apertures (8) in the wall of a lower mould part (3) on positions (8) opposite the recess (14) in the upper mould part (2).

- 2. Mould (1) as claimed in claim 1, characterised in that a plurality of suction runners (9) connecting onto the wall defining the mould cavity (10) are in mutual communication and are connected to a single runner (13) which connects onto a side of the lower mould part (3) remote from the mould cavity (10).
- 3. Mould (1) as claimed in claim 1 or 2, characterised in that the runner (9) debouches in a wall defining a mould cavity (10), which wall is screened from a feed opening for encapsulating material by a carrier when encapsulating material is fed to the mould (1).
- 4. Mould (1) as claimed in any of the foregoing claims, characterised in that the apertures (8) are arranged in the wall of a lower mould part (3) in patterns.
- 5. Mould (1) as claimed in any of the foregoing claims, characterised in that
  the lower mould part (3) is provided with at least one aligning edge for positioning a
  carrier relative to the lower mould part (3).

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- 6. Encapsulating device for encapsulating electronic components mounted on a carrier, comprising:
- a mould (1) as claimed in any of the foregoing claims,
- drive means for positioning and causing the mould parts (2,3) to move relative to each other,
- feed means for encapsulating material, and
- a fan connecting onto the side of the runner (9) remote from the mould cavity (10), characterised in that the fan is adapted to blow gases into the runner (9) and to extract gases from the runner (9).

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- 7. Method for encapsulating electronic components mounted on a carrier, comprising the steps of:
- A) positioning at least one carrier relative to a lower mould part (3),
- C) closing a mould (1) by moving the lower mould part (3) and an upper mould part (2) towards each other,
  - D) feeding encapsulating material to the part of the mould cavity (10) left clear by the carrier, and
  - D) opening the mould halves (2,3) and removing the carrier with encapsulating material arranged thereon, wherein during step D) an overpressure is applied in a runner (9) connecting onto a wall co-defining the mould cavity (10), which overpressure releases the encapsulated electronic components from the lower mould part (3).
- 8. Method for encapsulating electronic components according claim 7,
  25 characterised in that after step A) during a step B) an underpressure is applied in
  the runner (9), whereby the carrier is sucked to lower mould part (3).



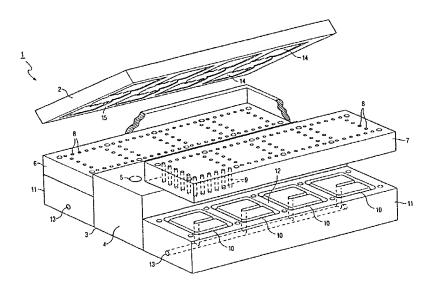
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## INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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(21) International Application Number: PCT/NL (22) International Filing Date: 5 July 1999 (		l	(81) Designated States: JP, KR, SG, US, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).				
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(71) Applicant (for all designated States except US): FI [NL/NL]; Ratio 6, NL-6921 RW Duiven (NL).	ІСО В.	.v.					
(72) Inventors; and (75) Inventors/Applicants (for US only): TOMASSEN Gerardus, Antonius [NL/NL]; Zonegge 16–24, GN Zevenaar (NL). CLAASSEN, Antonius, [NL/NL]; Polderdijk 73b, NL–6914 KJ Herwen (	NL-69 Bernaro	103					
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(54) Title: MOULD, ENCAPSULATING DEVICE AND METHOD OF ENCAPSULATION

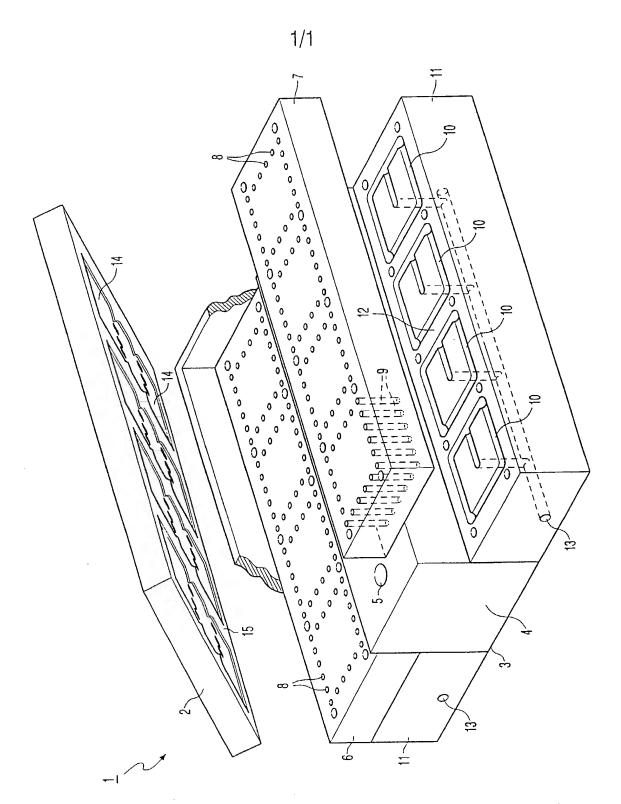


#### (57) Abstract

The invention relates to a mould for encapsulating electronic components mounted on a carrier, comprising: at least two mould parts displaceable relative to each other, at least one of which is provided with a recess, and feed means for encapsulating material, wherein at least one of the mould parts is provided with a runner which connects on one side to a wall of a mould part co-defining a mould cavity and connects on the other side to a side of the mould part remote from the mould cavity. The invention also relates to an encapsulating device of which such a mould forms part, and to a method for encapsulating electronic components mounted on a carrier.

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DECLAR (Includes R

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		ER OF ATTORNEY FOR PAT	ENT APPLICATION	ATTORNEY'S DOCKET NUMBER					
(Includes Referen	3135–010012								
ė.	As a be	clow named inventor, I heret	oy declare that:						
My	residence, p	ost office address and citizens	ship are as stated below next to	my name.					
invo	entor (if plur	e original, first and sole inver al names are listed below) of vention entitled:	ntor (if only one name is listed t the subject matter which is clai	pelow) or an original, first and joint med and for which a patent is					
	Mould, encapuslating device and method of encapsulation								
the	the specification of which (check only one item below):								
		is attached hereto							
		was filed as United States	application	-					
		Serial No.							
		on							
		and was amended							
		011		(if applicable).					
	99	was filed as PCT internati	onal application Number PCT/	NL99/00424					
		on 05 July 1999							
		and was amended under I	PCT Article 19						
		on		(if applicable).					
the	claims, as a	nended by any amendment r		e-identified specification, including					
		1 Title 37, Code of Federal Re							
pat tha pat tha	ent or invent n the United ent or invent n the United	tor's certificate or of any PC' States of America listed belo tor's certificate or any PCT in	F international application(s) de w and have also identified below nternational application(s) design	19 of any foreign application(s) for esignating at least one country other w any foreign application(s) for guating at least one country other tving a filing date before that of the					
PRIOR FOR	EIGN/PCT	APPLICATION(S) AND AN	Y PRIORITY CLAIMS UNDE	R 35 U.S.C. 119:					
	JNTRY dicate "PCT")	APPLICATION NUMBE	DATE OF FILING (day, month, year)	PRIORITY CLAIMED UNDER 35 USC 119					
	NL.	1009563	06 July 1998 (06.07.98)	YES NO					
				☐ YES NO					
PTO 1391 (rev 10 83)			Page 1 or 2	S DEPARTMENT OF COMMERCE Parent and crademark Office					

(Combined Declaration For Patent Application and Power of Attorney --PTO 1391 [13-11]--page 1 of 2)

СО	MBINED DECLARAT	TION FOR PATENT APPLIC	ATION AND PO	WER OF AT	TORNEY (CONTINU	ED)	ATTORNE	Y DOCKET NUMBER		
(Jn	eludes Reference to I hereby internatio subject r manner p	o PCT International App claim the benefit under onal application(s) designatter of each of the clorovided by the first par information as defined te of the prior application	r Title 35, Un mating the Un aims of this a agraph of Title in Title 37, C	ited States nited States pplication a 35, Unite Code of Fed	Code, \$120 of an of America that is not disclosed it disclosed. \$1 deral Regulations.	y United States a is/are listed below in that/those prior 12, I acknowledge §1.56(a) which c	and, insotar a application(s) the duty to discourred between	n the sclose		
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2	FULL NAME	FAMILY NAME		NAMES				•		
0	OF INVENTOR  RESIDENCE & CITIZENSHIP	TQMASSEN CITY ZEVENAAR	STA	Marcel, Gerardus, Antonius  STATE OR FOREIGN COUNTRY  The Netherlands				COUNTRY OF CITIZENSHIP The Netherlands		
1	POST OFFICE ADDRESS	POST OFFICE ADDRESS Zonegge 16-24	crty							
2	FULL NAME OF INVENTOR	FAMILY NAME CLAASSEN		NAMES	rnardus					
0	RESIDENCE & CITIZENSHIP	CITY Herwen	STATE	Antonius, Bernardus STATE OR FOREIGN COUNTRY The Netherlands // / Cny Herwen				COUNTRY OF CITIZENSIUP The Netherlands		
2	POST OFFICE ADDRESS	POST OFFICE ADDRESS Polderdijk 73b	Her				The Ne 6914 K	STATE & ZIP CODE/COUNTRY The Netherlands 6914 KJ		
	I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statement may jeopardize the validity of the application or any patent issuing thereon.									
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